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<u>L24</u>	L20 and 111	16	<u>L24</u>
<u>L23</u>	L20 and 110	0	<u>L23</u>
<u>L22</u>	L20 and 19	2	<u>L22</u>
<u>L21</u>	L20 and 17	79	<u>L21</u>
<u>L20</u>	14 and load and store	125	<u>L20</u>
<u>L19</u>	14 and 111	19	<u>L19</u>
<u>L18</u>	14 and 110	0	<u>L18</u>
<u>L17</u>	14 and 19	2	<u>L17</u>
<u>L16</u>	14 and 17	84	<u>L16</u>
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<u>L14</u>	11 and 110	36	<u>L14</u>

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<u>L13</u>	11 and 19	7	<u>L13</u>
<u>L12</u>	11 and 17	278	<u>L12</u>
<u>L11</u>	(711/201-221)[CCLS]	5810	<u>L11</u>
<u>L10</u>	(717/106-161)![CCLS]	7263	<u>L10</u>
<u>L9</u>	(712/223,224,230)[CCLS]	437	<u>L9</u>
<u>L8</u>	(712/2-300)[CCLS]	11774	<u>L8</u>
<u>L7</u>	(712/2-300)![CCLS]	11774	<u>L7</u>
DB=B	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L6</u>	L3 near45 11	1	<u>L6</u>
<u>L5</u>	L3 near25 11	1	<u>L5</u>
<u>L4</u>	L3 and 11	158	<u>L4</u>
<u>L3</u>	compar\$7 near12 address\$5 near15 (symbol\$5 or form\$1 or format\$5 or operand\$1 or field\$1)	5819	<u>L3</u>
<u>L2</u>	L1 near18 compar\$6	35	<u>L2</u>
<u>L1</u>	(without or no or "NOT") near5 (comput\$6 or calculat\$5) near8 address\$3	2350	<u>L1</u>

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ieee jnl	IEEE Journal or Magazine	Select	Article Infermation	
IEE JNL	IEE Journal or Magazine	} ;	Content-addressable and associative memory: alternatives to the ubiquitous RAM	
HEEE CMF	IEEE Conference Proceeding		Chisvin, L.; Duckworth, R.J.;	
IEE CNF	IEE Conference Proceeding		Computer Volume 22, Issue 7, July 1989 Page(s):51 - 64	
HEHE STO	IEEE Standard		Digital Object Identifier 10.1109/2.30732	
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